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D.C. 20231, on July 1, 2002.

Jeffrey L. Costellia

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shunpei YAMAZAKI et al.

) Examiner: William D. Coleman

Group Art Unit: 2823

Serial No. 09/436,984

Examiner: William D. Colei

Filed: November 9, 1999

) Date: July 1, 2002

MANUFACTURING METHOD THEREOF

SEMICONDUCTOR DEVICE AND

RESPONSE TO ELECTION OF SPECIES

Commissioner for Patents Washington, D.C. 20231

Sir:

For:

In response to the election requirement in the Office Action May 30, 2002, Applicants hereby elect with traverse Group II, directed to a generic MOSFET, in which claims 31-55 are readable thereon.

Applicants wish to point out that in paragraph 3 it is stated that Group II (claims 31-55) are designated as being generic to Group I (claims 1-14). Therefore, if Group II is elected and these claims all remain generic, it would seem that the Examiner would then be required to examine the more specific claims of Group I, if it is believed by the Examiner that the generic claims are not patentable over the prior art. In any event, if generic claims of Group II are ultimately allowed, then the claims of non-elected Group I should also be allowed in the instant application.

Consideration and allowance of the instant application are now respectfully requested.

Respectfully submitted,

Jeffrey L. Costellia

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